Simulator Requirement Specification

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| Department Name | Electrical Engineering |
| Class | TY BTech |
| Semester | V |
| Subject Name | Microprocessor and microcontroller |
| Experiment No. | 1 |
| Experiment Name | Block transfer& block exchange operation using 8051 |

Version History

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| --- | --- | --- | --- | --- |
| Sr. No. | Version Number | Created By | Approved By | Date |
| 1 | v1.0 | Yuvraj Bhalekar | Mrs. Sushmita Amit Sarkar | 12/10/2020 |
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Detailed Requirement Specification

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| **Req. ID** | **Visual Entity Required** | **Requirement Description** | **Comments** |
| REQ1 | Next Button | To move to next Instruction the instruction get highlighted and explanation will appear in front of it |  |
| REQ2 | Instruction window | The instructions will appear in this part and instruction window occupy half of the screen |  |
| REQ3 | Output window | Output will appear in this window which is another half of the window |  |
| REQ4 | Run button | to run code |  |
| REQ5 | Input fields | These are placed in the output window | Input field are provided to all the require registers, memory locations etc.  No need to provide for those which are not used in the instructions |
| REQ6 | Clear button | To clear all the output window values and set to initial values i.e. zero |  |
| REQ7 |  |  |  |